

Notice of References Cited	Application/Control No. 09/752,642	Applicant(s)/Patent Under Reexamination FLAKE ET AL.	
	Examiner Ronald D. Hartman Jr.	Art Unit 2121	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	A mixed-Language Simulator for Concurrent Engineering; Burgoon et al; March 1998; Verilog HDL Conference 1998.
	V	
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	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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